

APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Type:: REGULAR
Subject Matter:: UTILITY
CD-ROM or CD-R?: NONE
Title:: INSULATED GATE BIPOLAR
TRANSISTOR
Attorney Docket Number:: 251173US-2CONT
Total Drawing Sheets:: 15

INVENTOR INFORMATION

Applicant Authority Type:: INVENTOR
Primary Citizenship Country:: Japan
Status:: FULL CAPACITY
Given Name:: Tadashi
Family Name:: MATSUDA
City of Residence:: Yokohama-shi
Country of Residence:: Japan
Street of Mailing Address:: 304, 2-21, Komaoka 3-chome, Tsurumi-
ku,
City of Mailing Address:: Yokohama-shi
State or Province of Mailing Address:: Kanagawa
Country of Mailing Address:: Japan
Postal or Zip Code of Mailing Address:: 230-0071

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Continuation of	10/298,514	11/19/02

FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
2001-393722	Japan	12/26/01	YES

ASSIGNMENT INFORMATION

Assignee Name::	KABUSHIKI KAISHA TOSHIBA
Street of Mailing Address::	1-1, Shibaura 1-chome Minato-ku,
City of Mailing Address::	Tokyo
Country of Mailing Address::	Japan
Postal or Zip Code of Mailing Address::	105-8001